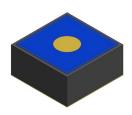


This die specification contains the basic features of the 3rd generation ThinGaN 10 mil dice from OSRAM Opto Semiconductors. Remarkable light extraction is reached by a particular top emitting design with vertical chip structure. Furthermore the LED die shows excellent reliability behaviour and optimized current spreading



Features

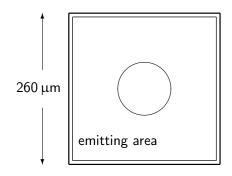
- Polarity n-side up
- Top emitting device
- Lambertian radiation
- Optimized for SMT applications
- Grouping: radiant power, wavelength

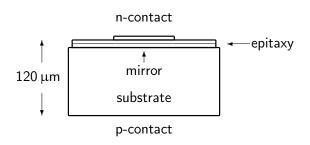
Applications

- Solid state lighting
- LCD backlighting
- Lamps
- Displays
- Light indicators

Ordering information: C4L-D10G3-1923-X (440 ... 465 nm, 16 ... 50 mW)

Delineation





Mechanical characteristics

DESCRIPTION		MINIMUM	$Typical^1$	MAXIMUM		
Chip size	(µm)	240	260	280		
Chip height	(µm)	110	120	130		
Bond pad diameter	(μm)	85	92	100		
Left contact		Cathode (n), gold				
Right contact		Anode (p), gold				
Die attach		Epoxy bonding				

C4L-D10G3





Electro-optical characteristics $(T_A=25^{\circ}\text{C})^2$

PARAMETER	Symbol	Condition	Min.	Typ. ¹	Max.	Unit
Forward voltage Dominant wavelength Radiant power Reverse voltage	$V_F \ \lambda_{dom} \ \Phi_e \ V_R$	$I_F=$ 20 mA $I_F=$ 20 mA $I_F=$ 20 mA $I_R=$ 10 μ A	2.80 440 16 5	3.00 25	3.40 465 50	V nm mW V

Maximum ratings $(T_A=25^{\circ}\mathrm{C})^3$

Parameter	Symbol	VALUE	Unit
Operating temperature range	T_{op}	-40 125	$^{\circ}C$
LED junction temperature	T_j	125	$^{\circ}C$
Forward minimum current	I_F	3	mΑ
Forward maximum current	I_F	50	mΑ
Forward peak current ($t < 10 \mu s; D = 0.005; T_S = 25^{\circ}C$)	I_{FM}	300	mA

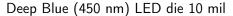
Binning $(I_F = 20 \,\mathrm{mA})^4$

	WAV	ELENGT	H (NM))				
	440-	442.5-	445-	447.5-	450-	452.5-	455-	457.5
	445	447.5	450	452.5	455	457.5	460	462.5
> 16	V19	VM19	W19	WM19	X19	XM19	Y19	YM19
> 20	V20	VM20	W20	WM20	X20	XM20	Y20	YM20
> 25	V21	VM21	W21	WM21	X21	XM21	Y21	YM21
> 32	V22	VM22	W22	WM22	X22	XM22	Y22	YM22
> 40	V23	VM23	W23	WM23	X23	XM23	Y23	YM23
	> 20 > 25 > 32	 440- 445 > 16 V19 > 20 V20 > 25 V21 > 32 V22 	440- 442.5- 445 447.5 > 16 V19 VM19 > 20 V20 VM20 > 25 V21 VM21 > 32 V22 VM22	440- 442.5- 445- 445 447.5 450 > 16 V19 VM19 W19 > 20 V20 VM20 W20 > 25 V21 VM21 W21 > 32 V22 VM22 W22	445 447.5 450 452.5 > 16 V19 VM19 W19 WM19 > 20 V20 VM20 W20 WM20 > 25 V21 VM21 W21 WM21 > 32 V22 VM22 W22 WM22	440- 442.5- 445- 447.5- 450- 445 447.5- 450- 452.5- 455- > 16 V19 VM19 W19 WM19 X19 > 20 V20 VM20 W20 WM20 X20 > 25 V21 VM21 W21 WM21 X21 > 32 V22 VM22 W22 WM22 X22	440- 442.5- 445- 447.5- 450- 452.5- 445 447.5- 450- 452.5- 455- 457.5- > 16 V19 VM19 WM19 X19 XM19 > 20 V20 VM20 WM20 X20 XM20 > 25 V21 VM21 W21 WM21 X21 XM21 > 32 V22 VM22 W22 WM22 X22 XM22	440- 442.5- 445- 447.5- 450- 452.5- 455- 445 447.5- 450- 452.5- 455- 460- > 16 V19 VM19 WM19 X19 XM19 Y19 > 20 V20 VM20 W20 WM20 X20 XM20 Y20 > 25 V21 VM21 W21 WM21 X21 XM21 Y21 > 32 V22 VM22 W22 WM22 X22 XM22 Y22

The binning table does not represent the actual sorting specifications. It is a general classification for wavelength and brightness.

The values shown in the diagrams above, represent the chip in a TOPLED package.

C4L-D10G3





Handling and Storage Conditions

Storage time for wafers in sealed condition shall not exceed 6 months (storage ambient conditions: $T_a=15\dots30^{\circ}C$; relative humidity: < 60%). The hermetically sealed shipment lot shall be opened under temperature and moisture controlled cleanroom environment only. Customers have to follow the according rules for desposition as the material can be hazardous for humans and the environment. Chips are placed on a blue foil, which may contain the following substance in a concentration of circ.18% wt: Bis (2-ethyl(hexyl)phthalate) (DEHP) [CAS #: 117-81-7; EC # 204-211-0]. Dice have to be handled ESD sensitive.

Packing

Chips are placed on a blue foil inside a 6 inch ring or alternatively on a blue foil (mylar). For shipment the wafers of a shipment lot are arranged to stacks. The stack is put in a plastic ESD bag with maximum of 14 wafers in one bag. Maximum of 4 bags is put in a packaging box. Maximum of 5 packaging boxes is put in a shipping carton which is sealed for storage and shipment. Please use the recycling operators familiar to you. If required you can ask for our help. Please get in touch with your nearest sales office. By agreement we will take packing material back, if sorted. Transport costs of any kind must be paid by customers. For packing material that is returned to us unsorted or which we are not obliged to accept, any costs incurred will be invoiced to you.

Design Objectives

The chip design was developed and released based on the producer's standard assembly procedures and packaging. Bond strength properties are in accordance to MIL-STD-750D, method 2037. Whether the chip fits to the customer's products with its according die and wire bond procedures and packaging must be evaluated by the customer himself. If workability problems arise after this release a mutually conducted problem solving procedure has to be set up, if the chips are suspected of contributing to the problems. The chips are produced with best effort, but on chip level a subset of the chip characteristics can be determined only. Performance of the chip in the customer's products can only be determined by the customer himself.

Returns and Complaints

For complaints and returns of material a RMA-number is necessary. Samples for analysis purposes can be send to us without credit.

Shipping Conditions

If not otherwise arranged, the "General Terms of Business of Chips 4 Light GmbH" apply for any shipment. If this document is not familiar to you, please request it at our nearest sales office.

C4L-D10G3 Deep Blue (450 nm) LED die 10 mil



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- The information describes the type of component and shall not be considered as assured characteristics. Due to technical requirements components may contain dangerous substances. For information on the types in question please contact our Sales Organization.
- Lead free product RoHS compliant.

C4L-D10G3

Deep Blue (450 nm) LED die 10 mil



- The quality level of the final visual inspection shall comply to an AQL of 1.0 (according to MIL-STD-105E, level II), if the customer performes an incoming visual inspection of a shipment.
- All chips are checked according to the producer's specification of the visual inspection. If this document is not familiar to you, please request it at our nearest sales office.

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¹Due to the special conditions of the manufacturing processes of LED, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.

²Measurements are done with an accuracy of $\pm 15\%$. Correlation to customer's equipment and products is required.

³Maximum ratings are package dependent and may differ between packages. The forward current is not limited by the die but by the effect of the LED junction temperature on the package. If you need more information on pulsed operation, please contact your next sales office about possible driving conditions. If not otherwise specified the maximum pulse current may not exceed the maximum current in continuous mode.

⁴There may be more than one bin on one single foil. Single bins cannot be ordered.

⁵A critical component is a component used in a life-support device or system whose failure can reasonably be expected to cause the failure of that life-support device or system, or to affect its safety or the effectiveness of that device or system.

⁶Life support devices or systems are intended(a) to be implanted in the human body,or(b) to support and/or maintain and sustain human life. If they fail, it is reasonable to assume that the health and the life of the user may be endangered.